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## Specifications and Applications Information

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The ERG *Smart Force Series* of LED Drivers are specifically designed for applications which require wide dimming and LCD brightness stability from a 5 Volt dc source. The SFDEA3980F is designed to provide backlight power for the Prime View PM070WT3 display.

Designed, manufactured and supported within the USA, the SFDE features:

- ✓ 8 mm or less in height
- ✓ Constant LED current
- ✓ External dimming
- ✓ High dimming ratio
- ✓ Soft start
- ✓ One year warranty

### Connectors

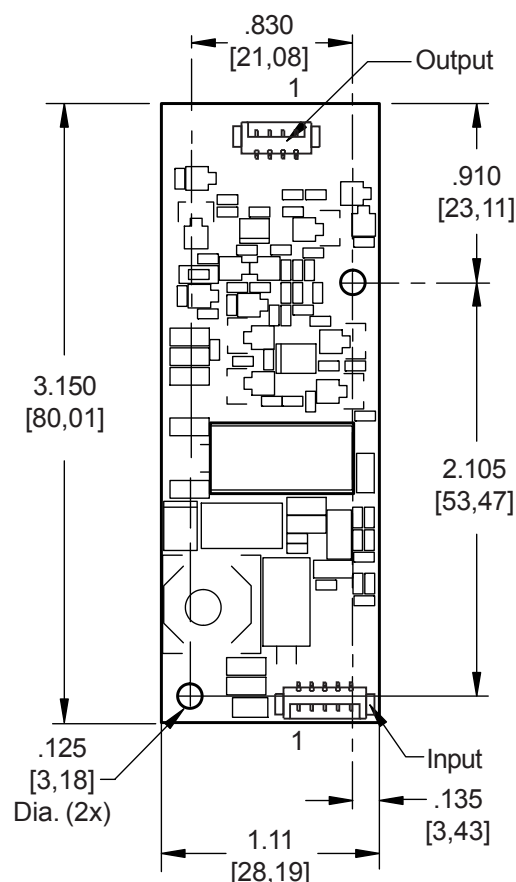
| Input Connector                                                   | Output Connector*                                                        |
|-------------------------------------------------------------------|--------------------------------------------------------------------------|
| Molex<br>53261-0571                                               | Molex<br>53261-0471                                                      |
| J1-1 Vin(+)<br>J1-2 Vin(+)<br>J1-3 GND<br>J1-4 GND<br>J1-5 Enable | J2-1 (do not use)<br>J2-2 (do not use)<br>J2-3 Cathode 1<br>J2-4 Anode 1 |
| * Requires harness:<br>ERG part number H10202304 recommended      |                                                                          |

# SFDEA3980F



## Smart Force LED Driver

### Package Configuration



PCB components are shown for reference only. Actual product may differ from that shown.

Mass: 13 grams





## Absolute Maximum Ratings

| Rating               | Symbol       | Value         | Units |
|----------------------|--------------|---------------|-------|
| Input Voltage Range  | $V_{in}$     | -0.3 to +5.5  | Vdc   |
| Storage Temperature  | $T_{stg}$    | -40 to +85    | °C    |
| Enable Input Voltage | $V_{Enable}$ | 0 to $V_{in}$ | Vdc   |

## Operating Characteristics

Unless otherwise noted  $V_{in}$  = 5.00 Volts dc and  $T_a$  = 25°C.

| Characteristic                         | Symbol       | Min  | Typ  | Max  | Units |
|----------------------------------------|--------------|------|------|------|-------|
| Input Voltage                          | $V_{in}$     | +4.5 | +5.0 | +5.5 | Vdc   |
| Component Surface Temperature (Note 1) | $T_s$        | -40  | -    | +80  | °C    |
| Input Current                          | $I_{in}$     | 0.57 | 0.67 | 0.77 | Adc   |
| Peak Inrush Current (Note 2)           | $I_{peak}$   | -    | 2.5  | -    | Adc   |
| LED String Voltage                     | $V_{LED}$    | 9    | 10   | 11   | Vdc   |
| Efficiency                             | $\eta$       | -    | 66   | -    | %     |
| Output Current (per string)            | $I_{out}$    | 209  | 220  | 231  | mAdc  |
| <b>Enable Pin</b> (Note 3)             |              |      |      |      |       |
| Turn-on Threshold                      | $V_{thon}$   | -    | -    | 2.0  | Vdc   |
| Turn-off Threshold                     | $V_{thoff}$  | 0.8  | -    | -    | Vdc   |
| Enable Input Impedance (Note 4)        | $R_{Enable}$ | -    | 5    | -    | kOhms |

Specifications subject to change without notice.

Note 1 Surface temperature must not exceed 80°C. SOT89 package to be at or below 110°C. SOT89 package is 75°C at 25°C ambient temperature.

Note 2 Peak inrush occurs over a 1 to 3 ms time period, during initial startup.

Note 3 The input voltage to the driver must be within its operating characteristic before the driver is enabled, otherwise the driver may not start or may shut down unexpectedly.

Note 4 Input impedance is 5.0 kOhms to GND.



## Application Information

The ERG SFDEA3980F has been designed to be configured in multiple ways:

### NO DIMMING

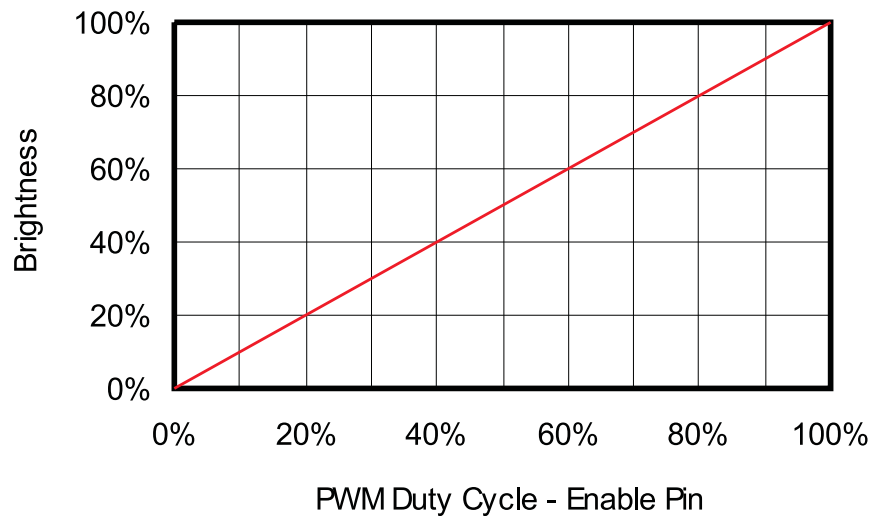
- OPERATION: The SFD driver can be configured to operate without dimming by pulling up the Enable Pin (J1-5).
- Pins 1 and 2 of connector J1 must be connected to +Vin, between 4.5 and 5.5 Vdc. Pins 3 and 4 of connector J1 must be connected to GND.

### EXTERNAL PWM DIMMING

- OPERATION: External PWM configuration as shown in Figure 1 allows the user to control display brightness with an externally generated PWM signal. The user is responsible to provide the PWM signal. A dimming ratio up to 5000:1 at  $\leq 200$  Hz is possible with this configuration.
- DIMMING: Dimming is accomplished by applying a PWM signal to the Enable Pin (J1-5). PWM on and off levels are specified in the Operating Characteristics section of the data sheet. Display brightness is modulated by controlling the PWM duty cycle as shown in Graph 1.
- Pins 1 and 2 of connector J1 must be connected to +Vin, between 4.5 and 5.5 Vdc. Pins 3 and 4 of connector J1 must be connected to GND.



## EXTERNAL PWM DIMMING



Graph 1

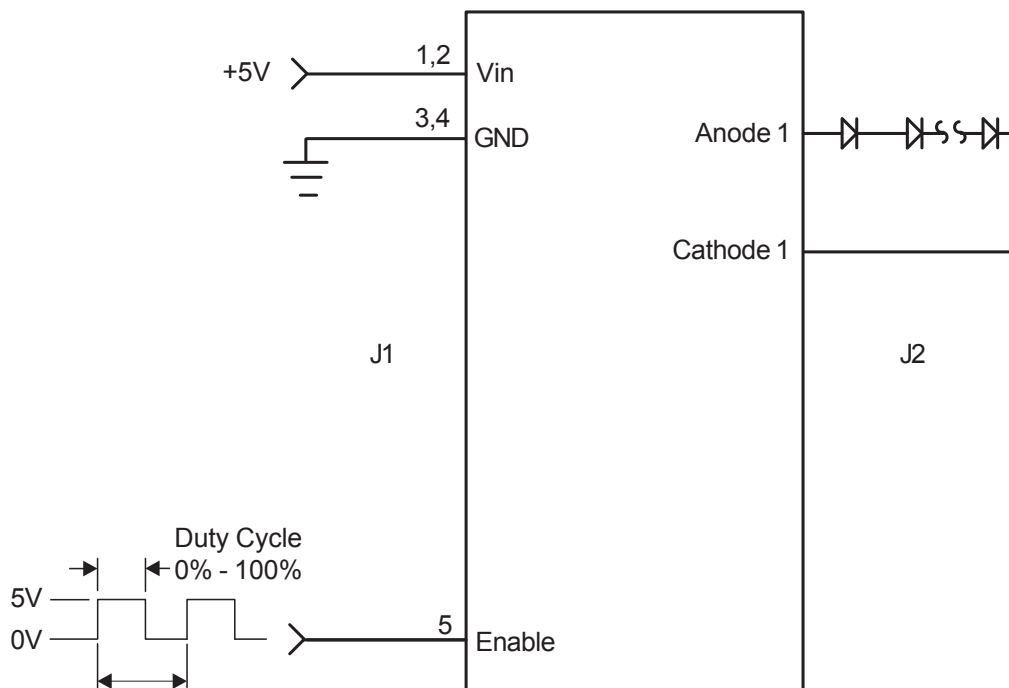


Figure 1



Endicott Research Group, Inc. (ERG) reserves the right to make changes in circuit design and/or specifications at any time without notice. Accordingly, the reader is cautioned to verify that data sheets are current before placing orders. Information furnished by ERG is believed to be accurate and reliable. However, no responsibility is assumed by ERG for its use.